



00862.022338.

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
: Examiner: D. Mason
NAOHISA SUZUKI, et al.)
: Group Art Unit: 2111
Application No.: 09/939,806)
: Filed: August 28, 2001)
: For: INFORMATION PROCESSING)
: APPARATUS AND METHOD)
: THEREFOR) August 4, 2004

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AUG 10 2004

Technology Center 2100

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. § 1.56, Applicants respectfully direct the Examiner's attention to the document listed below and on the enclosed Form PTO-1449. A copy of the document so listed is enclosed.

"IEEE Standard for a Higher Performance Serial Bus", IEEE 1394-1995,
IEEE Computer Society.

The foregoing document was discussed in the specification and might be deemed pertinent for the reasons given there.

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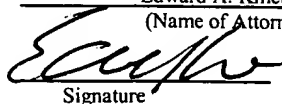
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

August 4, 2004
(Date of Deposit)

Edward A. Kmett (Reg. No.: 42,746)
(Name of Attorney for Applicant)


Signature

August 4, 2004
Date of Signature

The subject application has received an Office Action on the merits but has not yet received either a final action or a Notice Of Allowance. Accordingly, this Information Disclosure Statement is filed under 37 C.F.R. § 1.97(c) and is accompanied by the \$180.00 fee specified at 37 C.F.R. § 1.17(p).

The Examiner is urged to study this information in its entirety and to make an independent determination of the materiality of the information to the claimed invention. Additionally, the Examiner is requested to indicate that this information has been considered by initialing the appropriate portion of Form PTO-1449 and returning the initialed form to Applicant with the next communication.

Applicants' undersigned attorney may be reached in our Costa Mesa, California office at (714) 540-8700. All correspondence should continue to be directed to our below-listed address.

Respectfully submitted,



Attorney for Applicants
Edward A. Kmett
Registration No. 42,746

FITZPATRICK, CELLA, HARPER & SCINTO
30 Rockefeller Plaza
New York, New York 10112-2200
Facsimile: (212) 218-2200

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICELIST OF REFERENCES CITED BY APPLICANT(S)
(Use several sheets if necessary)

APPLICANT

NAOHISA SUZUKI, et al.

FILING DATE

August 28, 2001

GROUP

2111

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

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FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/ OR ABSTRACT

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

		"IEEE Standard for a Higher Performance Serial Bus", IEEE 1394-1995, IEEE Computer Society.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.